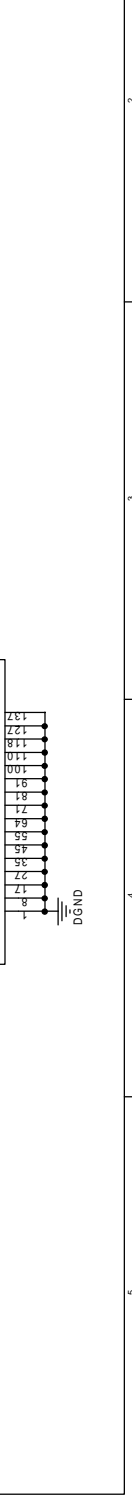
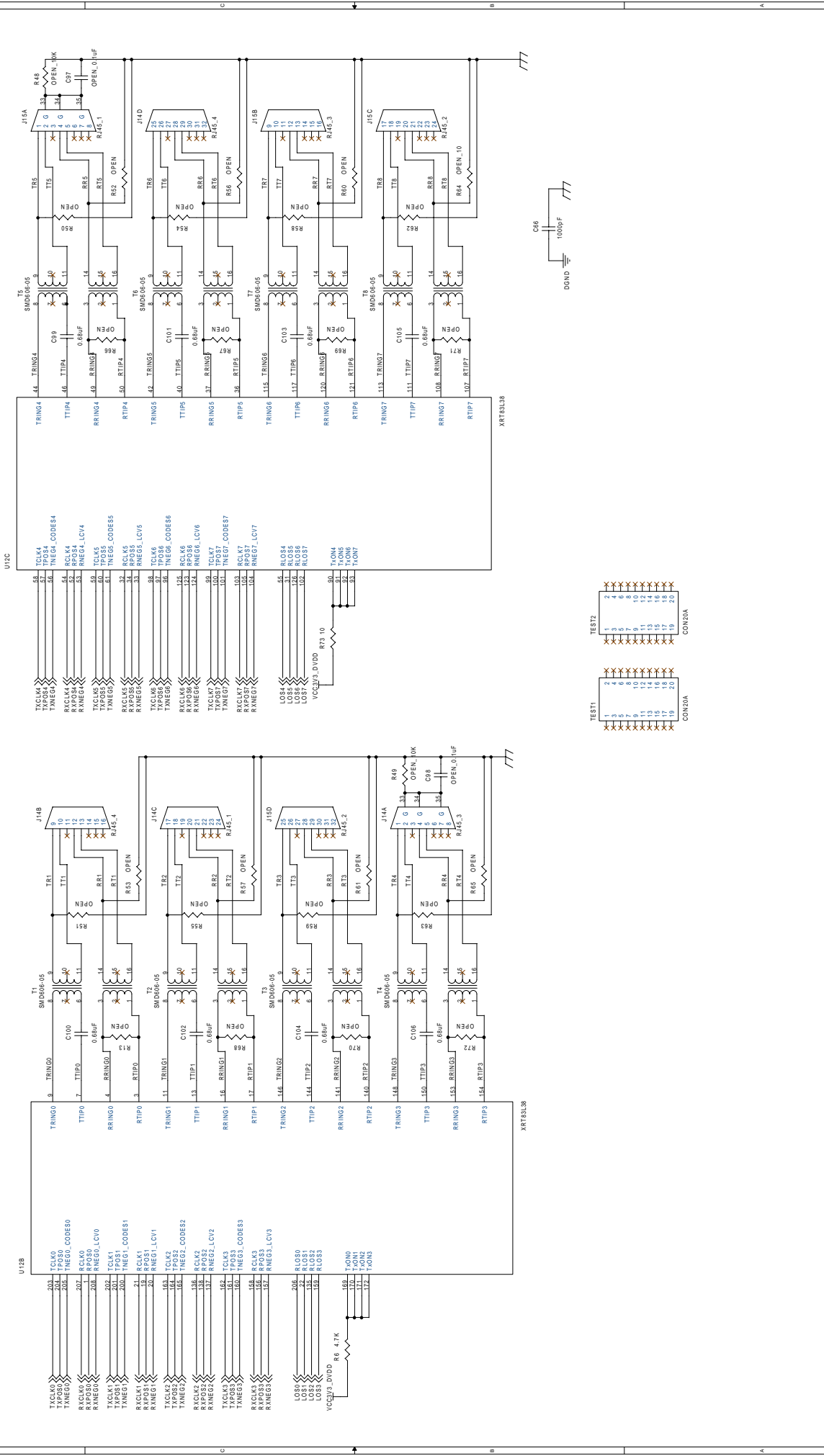


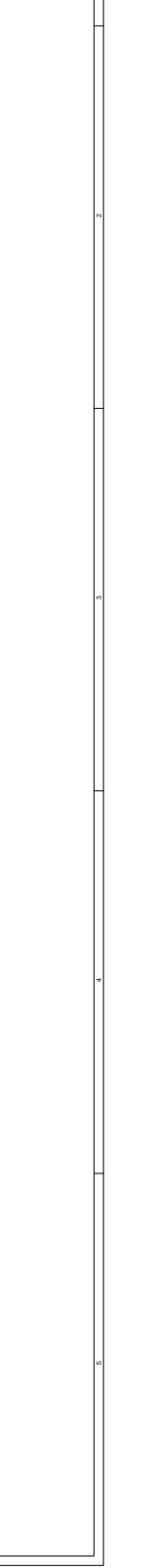
Title		XRT84L38/XRT89L38 EVALUATION BOARD	
S28	Document Number	XRT84L38 CHANNEL 4, 5 AND FPGA3	
B	Revision	1.0	
Date:	Wednesday, July 17, 2012	Sheet	4 of 8



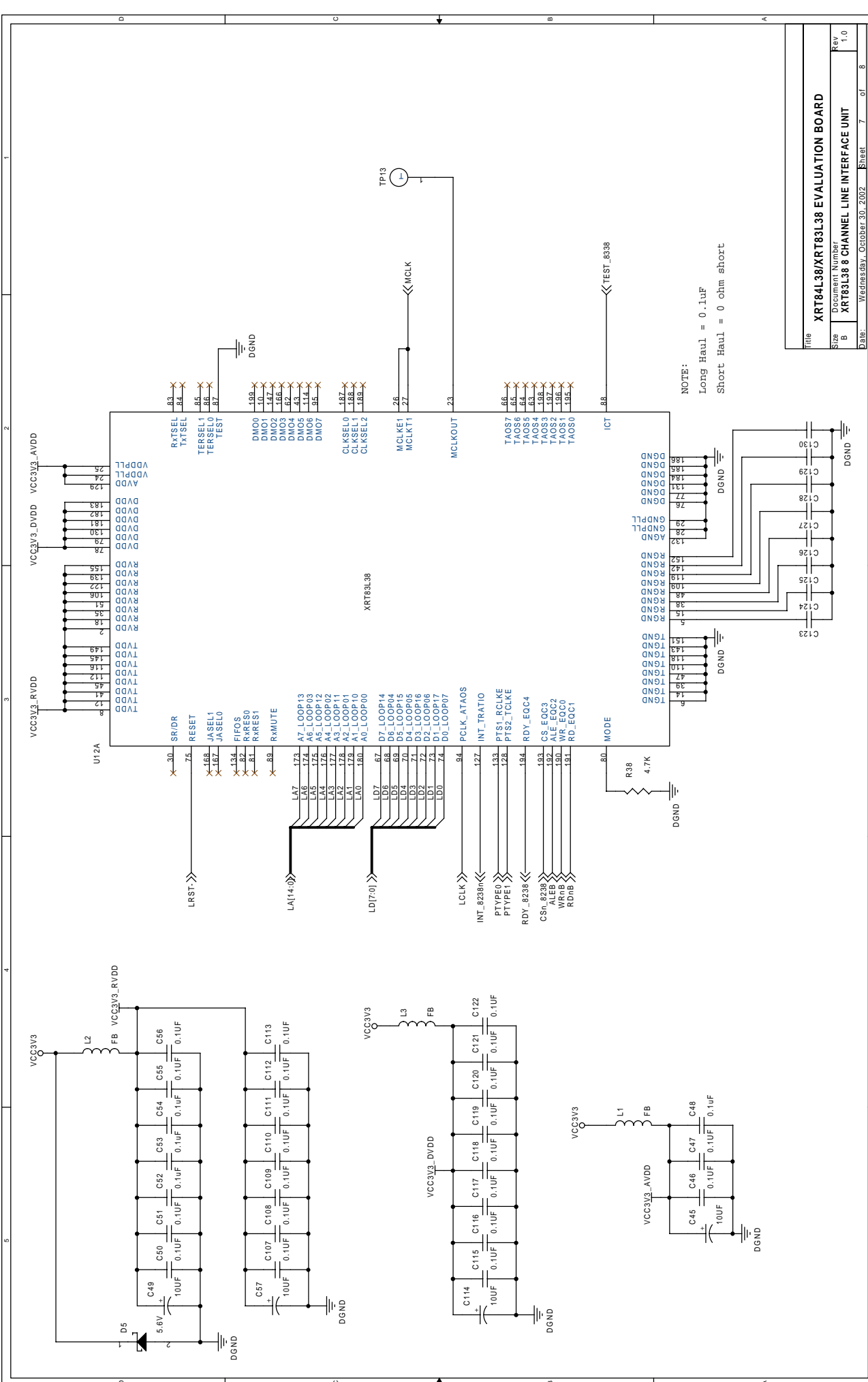




FILE	XRTS38/ARTS38 EVALUATION BOARD		
REV	C	XRTS38 8 CHANNEL LINE INTERFACE UNIT	
DATE	FRI01-NOV08 15:10:03	DESIGN	C_01_0
SHEET	10		

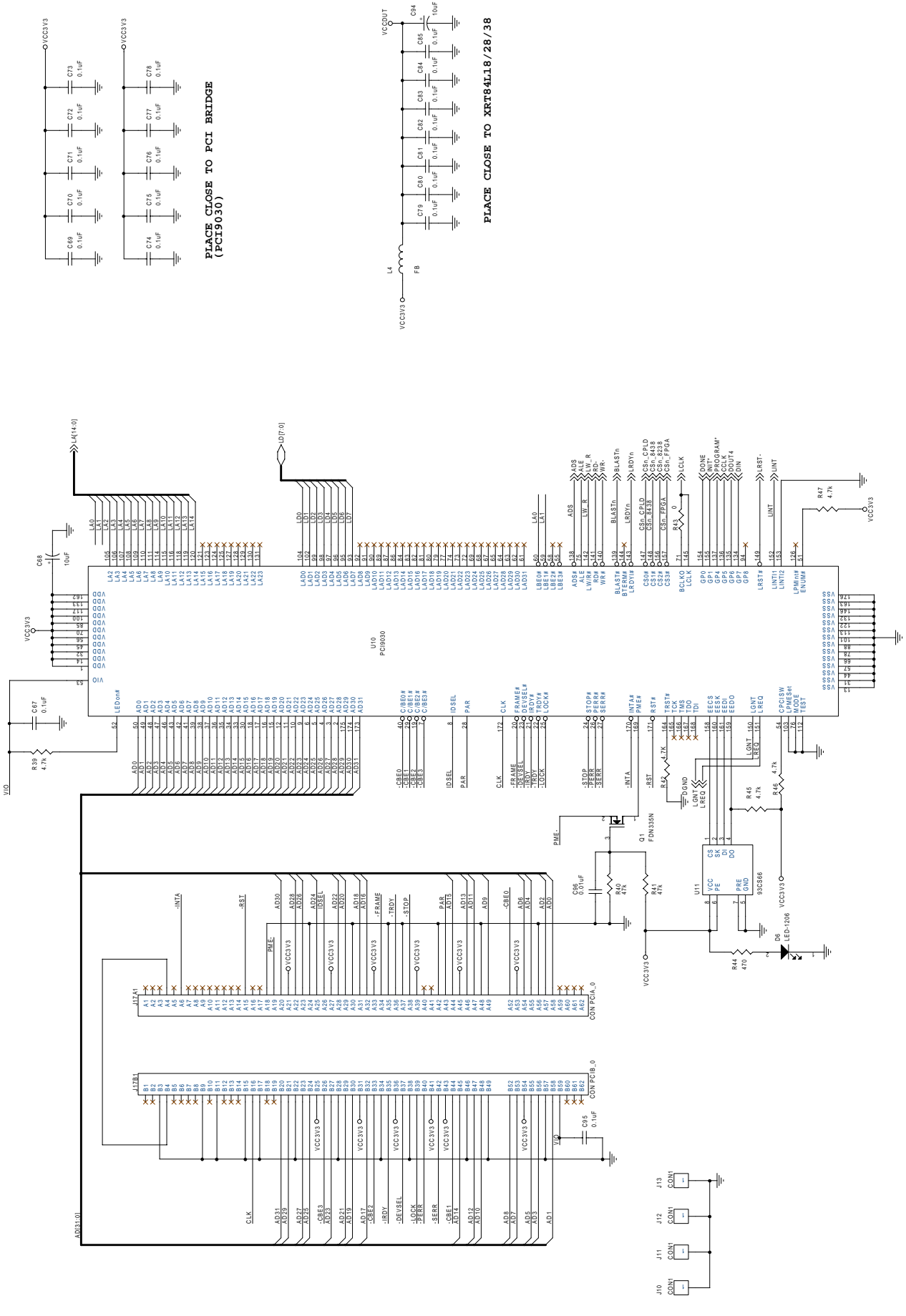


FILE	XRTS38/ARTS38 EVALUATION BOARD		
REV	C	XRTS38 8 CHANNEL LINE INTERFACE UNIT	
DATE	FRI01-NOV08 15:10:03	DESIGN	C_01_0
SHEET	10		



Title		<b>XRT84L38/XRT83L38 EVALUATION BOARD</b>	
Document Number		<b>XRT83L38 8 CHANNEL LINE INTERFACE UNIT</b>	
Rev	1.0	Sheet	7 of 8
Date:	Wednesday, October 30, 2002	Street	

NOTE:  
 Long Haul = 0.1uF  
 Short Haul = 0 ohm short



PLACE CLOSE TO PCI BRIDGE  
(PCI9030)

PLACE CLOSE TO XRT84L18/28/38